

FIG.

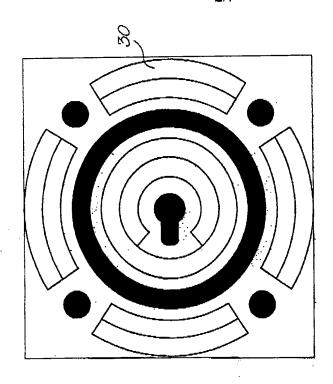
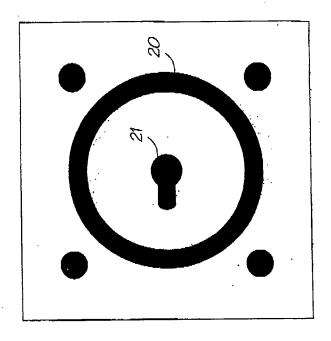
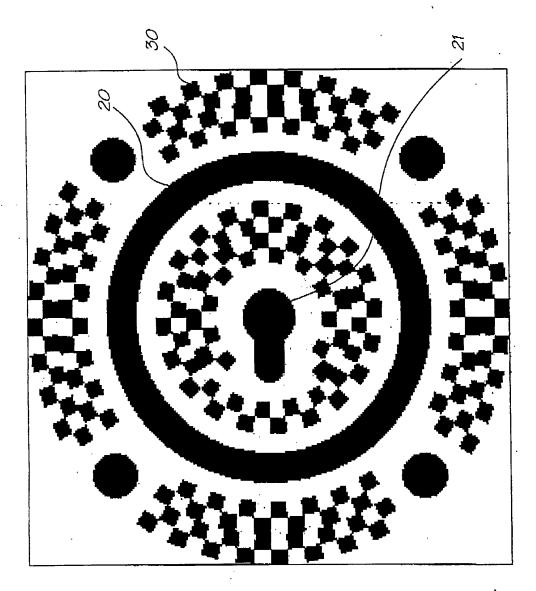


FIG. 3



F10.1



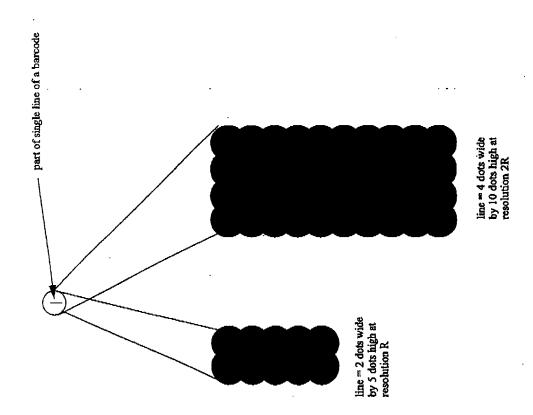


FIG. 5

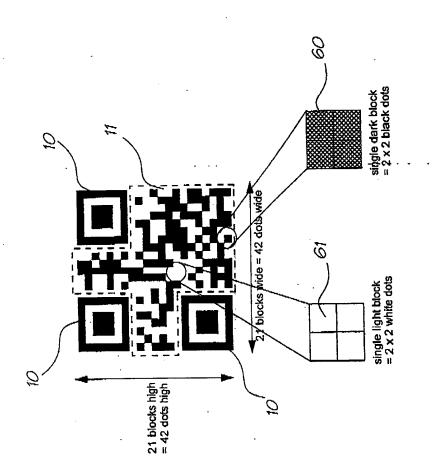
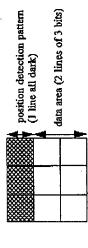


FIG. 0

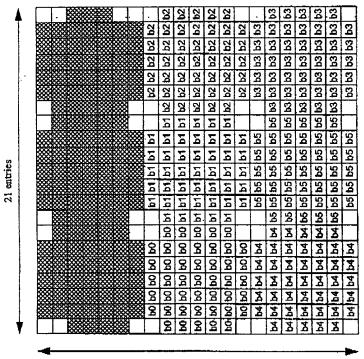


always l (background)	data bit 2	data bit 3
always I (background)	data bit 1	data bit 5
always l (background)	data bit 0	data bit 4

F10.7

Constant 0

Constant 1



21 entries

F16. B